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Vivekananda College of Engineering & Technology, Puttur [A Unit of Vivekananda Vidyavardhaka Sangha Puttur ®]

Affiliated to VTU, Belagavi & Approved by AICTE New Delhi

CRM08

Rev 1.10

CSE

29/07/2022

CONTINUOUS INTERNAL EVALUATION - 2

Dept:CSE	Sem / Div:4th CSE	Sub:Operating Systems	SCode:18CS43		
Date:04/8/22	Time: 9:30-11:00	Max Marks: 50	Elective:N		

Note: Answer any 2 full questions, choosing one full question from each part.

QN	1	Questions									Marks	KBT	CO's				
	1							P	ART	ΓΑ							-
	1000000	fine Pagram.	agin	g. I	Expl	lain	pag	ging	, ha	rdw	are	wit	.h a	neat	t 10	L2	CO2
+-	1. Ass	ssume that t			rocesse	s P0 th	uronep	P4 and	d 4 type	es of re	sourc	es. At t	ime T	o we have	10	L2	CO2
b		e following:	state:	_			Max				-	railable					I Total Tourist
	1	Process	Alloc	B	C	D	A	B	C	D	A	В	C	D			
		PO	A	0	1	2	0	0	1	2	1	5	2	0			
		P1	1	0	0	0	1	7	5	0							
1		P2	1	3	5	4	2	3	5	6]						
		P3	0	6	3	2	0	6	5	6	-						1
		P4 Apply Ban	nker's A	Algorith	thm to a	4 answer	the follo	6 lowing:		0	_						
C	Wh	at is de	i) If	lf a requ		om a pro	rocess P	P1(0,4,2						ons	5	L2	CO2
1 1		deadlo							OR								
1		at is se	egm	enta	ition	ı. E:	xpla	in v	vith	a n	eat	dia	grai	m and	10	L2	CO2
Ь	Proce	ores All	ocation	sn.		TN	Max				Ave	ailable			10	L2	CO
0	1	A		B	C	A		B	C	,	A	В		C	1	-	
11	PO	0		$\frac{\mathbf{B}}{0}$	2	0		0	4		1	0		2	1		
	PI	1		0	0	12	,	0	1		Ċ			1-	1		
11	P2	1	3	-	5	1	\longrightarrow	3	7		4						
1)	P3	6	7	3	12	8		4	2		1						
11	P4	1	7	4	3	1		5	7		i						
11		the need n	matrix	and c	1	te safe	segu	-	ising I	Banke	r's al	eorith	m. Me	ention the			
	above	e system is est be grant	s safe	or not	st safe,	If a re	equest	from	proces	ss P2	arrive	s for ((0,0,2), can the	2		
					17717										P	: 1 / 3	1

1	С	What is fragmentation. Explain its types.	5	L2	CO2
		PART B			
3	- 1	With a neat sketch explain structure of page table.(all types)	10	L2	CO3
	b	Process Allocation Max Available	10	L2	CO3
	С	Example: Given the memory partitions of 200K, 700K, 500K, 300K, 100K, and 400K. Apply first fit ,best fit and the worst fit algorithms to place processes with 315K, 427K, 250K, and 550K size.	5	L2	CO3
		OR			
4	a	With a neat diagram explain Copy-on-write, Swapping and TLB.	10	L2	COS
	b	What is page fault? With a neat diagram explain steps in handling page fault.	10	L2	COE
	С	On a system using simple segmentation, compute the physical addresses for each of the logical address, logical address is given in the following segment table. If the address generates a segment fault, indicate it as "segment fault".	5	L2	CO3
-		Segment Base Length			
-		0 330 124 1 876 211			
1		2 111 99			
		3 498 302			
		i) 0,99 ii) 2,78 iii) 1,265 iv) 3,222 v) 0,111			

Prepared by: Mrs. Bharathi K/Mr.Raghavendra T.K

HOD